

SSC8123GS6A

P-Channel Enhancement Mode MOSFET

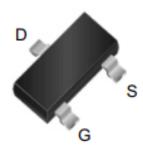
> Features

V _{DS}	V _{GS}	R _{DS(ON)} Typ.	ID
-20V	+12V	20mΩ@-4V5	-7A
	<u> </u>	25mΩ@-2V5	-78

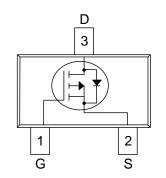
> Description

This device is produced with high cell density DMOS trench technology, which is especially used to minimize on-state resistance. This device particularly suits low voltage applications such as portable equipment, power management and other battery powered circuits.

Pin configuration







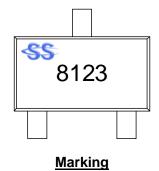
> Applications

- Load Switch
- Portable Devices
- DCDC Conversion

> Ordering Information

Device	Package	Shipping	
SSC8123GS6A	SOT-23-3L	3000/Reel	

Pin Configuration (Top View)







Symbol	Parameter	Ratings	Unit
V _{DSS}	Drain-to-Source Voltage	-20	V
V _{GSS}	Gate-to-Source Voltage	±12	V
ID	Continuous Drain Current ^a	-7	А
Ідм	Pulsed Drain Current ^b	-21	А
Po	Power Dissipation ^c	1.6	W
TJ	Operation junction temperature -55~150		°C
T _{STG}	Storage temperature range	-55~150	°C

> Absolute Maximum Ratings ($T_A=25^{\circ}$ unless otherwise noted)

> Thermal Resistance Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
Reja	Junction-to-Ambient Thermal Resistance ^a	78	°C/W

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25 °C.The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.



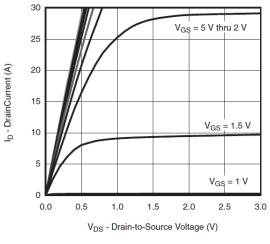


> Electrical Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)

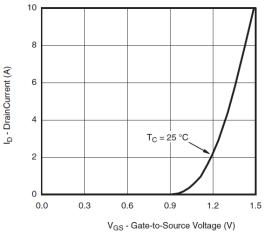
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = -250 \mu A$	-20			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250 uA$	-0.4	-0.7	-0.9	V
Drain Course On Desistance	RDS(on)	$V_{GS} = -4.5V, I_D = -5A$		20	26	mΩ
Drain-Source On-Resistance		$V_{GS} = -2.5V, I_D = -3A$		25	36	
Zero Gate Voltage Drain Current	loss	$V_{DS} = -16V$, $V_{GS} = 0V$			-1	μA
Gate-Source Leak Current	Igss	$V_{GS} = \pm 12V$, $V_{DS} = 0V$			±100	nA
Transconductance	G _{FS}	$V_{DS} = -5V, I_D = -5A$		10		s
Forward Voltage	Vsd	$V_{GS} = 0V$, $I_S = -2A$		-0.78	-1.2	V
Input Capacitance	Ciss			1430		
Output Capacitance	Coss	$V_{DS} = -10V, V_{GS} = 0V,$		182		pF
Reverse Transfer Capacitance	C _{RSS}	f = 1MHz		160		
Turn-on Delay Time	T _{D(ON)}			11		
Rise Time	Tr	V _{GS} = -4.5V, V _{DS} = -10V,		18		
Turn-off Delay Time	T _{D(OFF)}	$R_L = 1.43\Omega, R_G = 3\Omega$		45		ns
Fall Time	T _f			23		
Total Gate Charge	Q_{G}			15		
Gate to Source Charge	Q _{GS}	$V_{GS} = -4.5V, V_{DS} = -10V,$		3		nC
Gate to Drain Charge	Q _{GD}	- I _D = -7A		4		



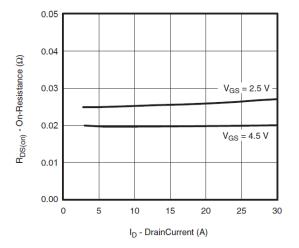
> Typical Performance Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)



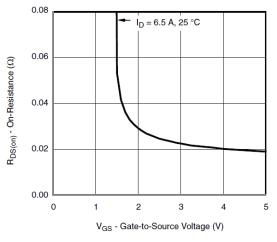




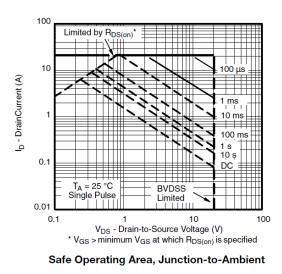
Transfer Characteristics

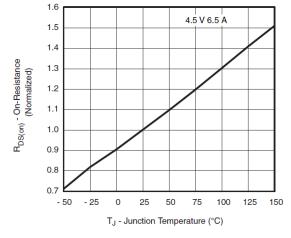


On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Gate-to-Source Voltage



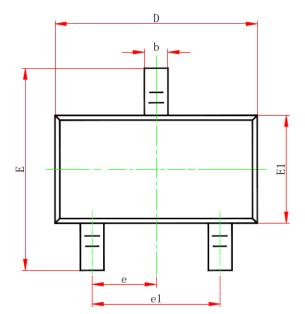


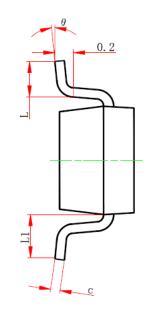
On-Resistance vs. Junction Temperature

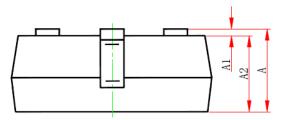
4 / 6



Package Information







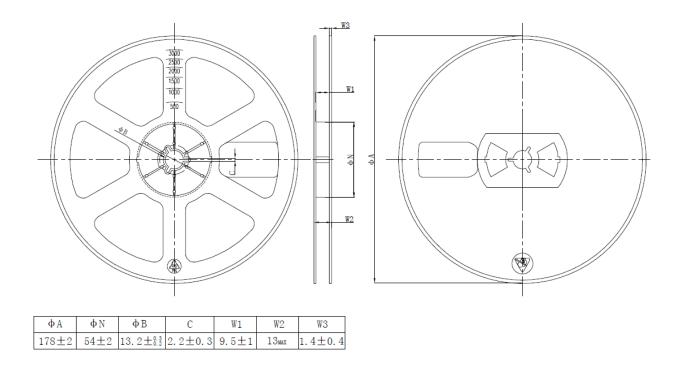
Package: SOT-23-3L

Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E1	1.500	1.700	0.059	0.067	
E	2.650	2.950	0.104	0.116	
е	0.950	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
L1	0.600REF.		0.024REF.		
θ	0°	8°	0°	8°	





Tape and Reel



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