

SSC8205BGSB

Common Drain N-Channel Enhancement Mode MOSFET

> Features

VDS	VGS	RDSON Typ.	ID
20V	1101/	13mR@4V5	7.0
ZUV	±12V	18mR@2V5	7A

Description

Advanced trench process technology. High density cell design for ultra-low on-resistance. High power and current handling capability. Fully characterized avalanche voltage and current.

Applications

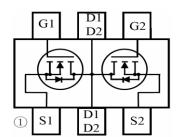
- Li-ion battery protection
- Load switch
- DCDC conversion

Ordering Information

Device	Package	Shipping	
SSC8205BGSB	SOT23-6L	3000/Reel	

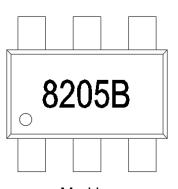
Pin configuration

Top view





SOT23-6L



Marking



➤ Absolute Maximum Ratings(T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	20	V
V _{GSS}	Gate-to-Source Voltage	±12	V
I _D	Continuous Drain Currentª	7	Α
I _{DM}	Pulsed Drain Current ^b	22	Α
P_D	Power Dissipation ^c	1.3	W
P _{DSM}	Power Dissipation ^a	0.7	W
TJ	Operation junction temperature	-55 to 150	°C
T_{STG}	Storage temperature range	-55 to 150	°C

➤ Thermal Resistance Ratings(T_A=25°C unless otherwise noted)

Symbol	Parameter	Typical	Maximum	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a		190	°C /\M
Rejc	Junction-to-Case Thermal Resistance		100	°C/W

Note:

- a. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper,in a still air environment with T_A =25°C. The value in any given application depends on the user is specific board design. The current rating is based on the t \leq 10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.



➤ Electronics Characteristics(T_A=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Unit
V _{(BR)DSS}	Drain-Source Breakdown Voltage	VGS=0V,ID=250uA	20			V
V _{GS} (th)	Gate Threshold Voltage	VDS=VGS,ID=250uA	0.5	0.65	1	V
-	Drain-Source On-	VGS=4.5V,ID=3A		13	15	Б
$R_{DS(on)}$	Resistance	VGS=2.5V,ID=2A		18	20	mR
I _{DSS}	Zero Gate Voltage Drain Current	VDS=16V,VGS=0V			1	uA
I _{GSS}	Gate-Source leak	VGS=±12V,VDS=0V			±100	nA
G _{FS}	Transconductance	VDS=5V,ID=4.5A		10		S
V _{SD}	Forward Voltage	VGS=0V,IS=1.25A		0.8	1.3	V
Ciss	Input Capacitance			600		
Coss	Output Capacitance	\/DQ_0\/_\/QQ_0\/_\		330		—
Crss	Reverse Transfer Capacitance	VDS=8V, VGS=0V, f=1MHz		140		pF
T _{D(ON)}	Turn-on delay time			8		
Tr	Rise Time	VGEN=4.5V,		10		
T _{D(OFF)}	Turn-off delay time	VDS=10V, RG=6R,ID=1A		35		ns
Tf	Fall Time			30		
Qg	Total Gate charge			10		
Qgs	Gate to Source charge	VGS=4.5V, VDS=10V, ID=6A		2.3		nC
Qgd	Gate to Drain charge			2.9		



> Typical Characteristics(T_A=25°C unless otherwise noted)

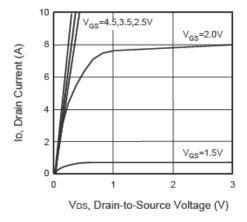


Figure 1. Output Characteristics

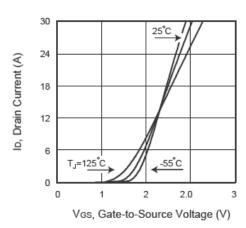


Figure 2. Transfer Characteristics

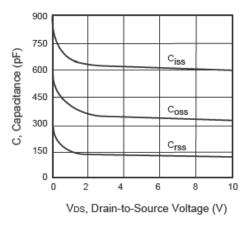


Figure 3. Capacitance

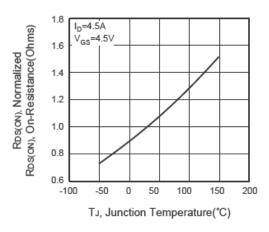


Figure 4. On-Resistance Variation with Temperature

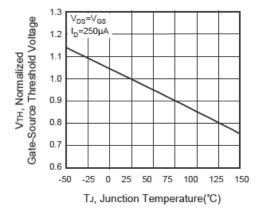


Figure 5. Gate Threshold Variation with Temperature

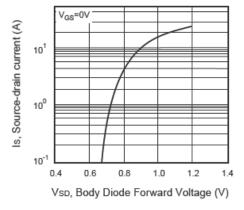


Figure 6. Body Diode Forward Voltage Variation with Source Current



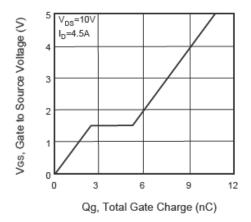


Figure 7. Gate Charge

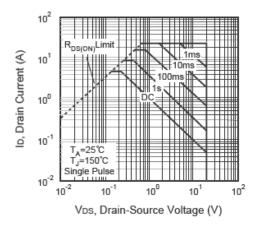


Figure 8. Maximum Safe Operating Area

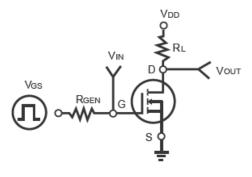


Figure 9. Switching Test Circuit

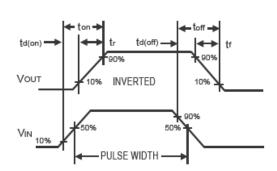


Figure 10. Switching Waveforms

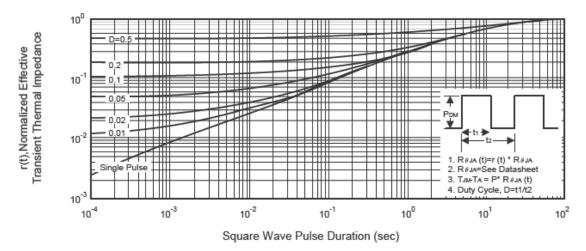
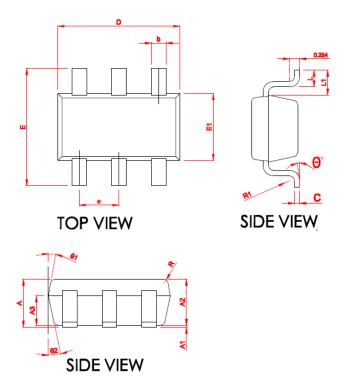


Figure 11. Normalized Thermal Transient Impedance Curve



> Package Information



	N	ILLIMETE	R
SYMBOL	MIN	NOM	MAX
Α	1.06	1.15	1.24
* A1	0.01	0.05	0.09
* A2	1.05	1.10	1.15
A3	0.65	0.70	0.75
* b	0.30	0.35	0.45
* с	0.117	0.127	0.157
* D	2.87	2.92	2.97
* E	2.72	2.80	2.88
* E1	1.55	1.60	1.65
* е	0.90	0.95	1.00
* L	0.32	0.40	0.48
* L1	0.55	0.60	0.65
R	0.10 REF		
R1	0.12 REF		
* θ	0	-	8°
θ1	8°	10°	12°
θ2	10°	12°	14°

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