



SSC8229GS6

P-Channel Enhancement Mode MOSFET

➤ Features

V_{DS}	V_{GS}	$R_{DS(ON)}$ Typ.	I_D
-20V	$\pm 12V$	22m Ω @-4V5	-5A
		29m Ω @-2V5	
		40m Ω @-1V8	

➤ Description

The SSC8229GS6 is P-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in load switch, electronic cigarette and Battery Isolation.

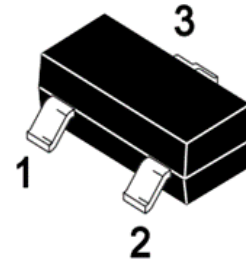
➤ Applications

- Load Switch
- Electronic Cigarette
- Battery Isolation

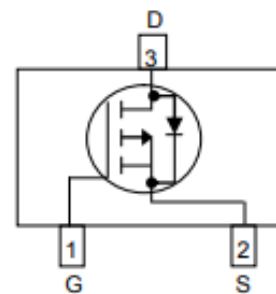
➤ Ordering Information

Device	Package	Shipping
SSC8229GS6	SOT-23	3000/Reel

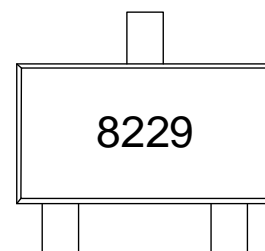
➤ Pin configuration



SOT-23



Pin Configuration (Top View)



Marking



➤ **Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain-to-Source Voltage	-20	V
V_{GSS}	Gate-to-Source Voltage	± 12	V
I_D	Continuous Drain Current ^a	-5	A
I_{DM}	Pulsed Drain Current ^b	-20	A
P_D	Power Dissipation ^c	0.89	W
P_{DSM}	Power Dissipation ^a	0.54	W
T_J	Operation junction temperature	-55~150	$^{\circ}\text{C}$
T_{STG}	Storage temperature range	-55~150	$^{\circ}\text{C}$

➤ **Thermal Resistance Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a	230	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	140	

Note:

- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user is specific board design. The power dissipation is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

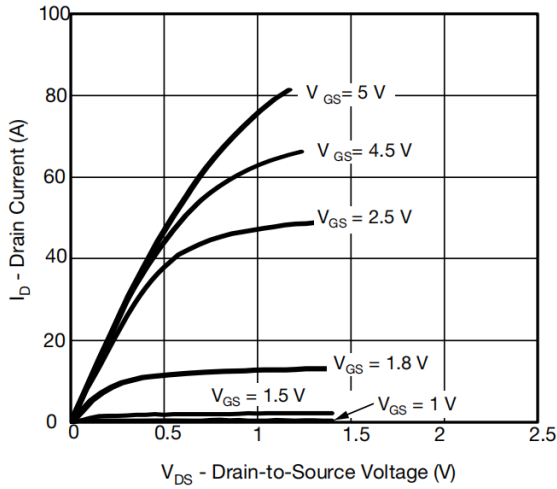


➤ **Electrical Characteristics (T_A=25°C unless otherwise noted)**

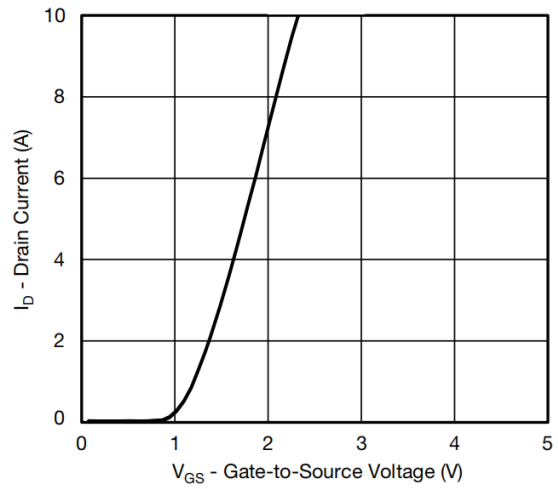
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250uA	-0.4	-0.7	-1	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = -4.5V, I _D = -5A		22	28	mΩ
		V _{GS} = -2.5V, I _D = -3A		29	38	
		V _{GS} = -1.8V, I _D = -2A		40	55	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -16V, V _{GS} = 0V			-1	μA
Gate-Source Leak Current	I _{GSS}	V _{GS} = ±12V, V _{DS} = 0V			±100	nA
Transconductance	G _{FS}	V _{DS} = -5V, I _D = -2A		26		s
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = -2A			-1.3	V
Input Capacitance	C _{ISS}	V _{DS} = -10V, V _{GS} = 0V, f = 1MHz		1963		pF
Output Capacitance	C _{OSS}			207		
Reverse Transfer Capacitance	C _{RSS}			204		
Turn-on Delay Time	T _{D(ON)}	V _{GS} = -4.5V, V _{DS} = -10V, R _L = 3Ω, R _G = 6Ω		38		ns
Rise Time	T _r			29		
Turn-off Delay Time	T _{D(OFF)}			141		
Fall Time	T _f			84		
Total Gate Charge	Q _G	V _{GS} = -4.5V, V _{DS} = -15V, I _D = -4A		22		nC
Gate to Source Charge	Q _{GS}			4		
Gate to Drain Charge	Q _{GD}			4.5		



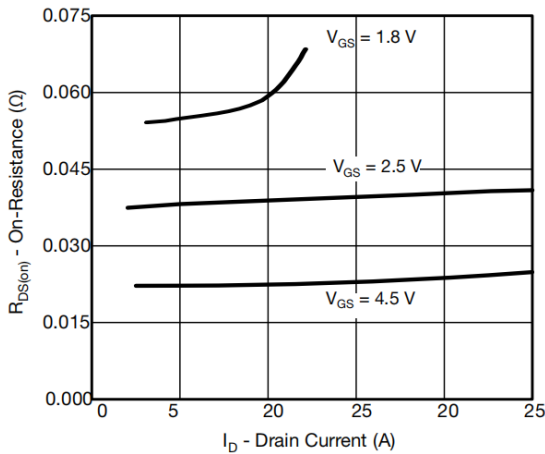
➤ **Typical Performance Characteristics (T_A=25 °C unless otherwise noted)**



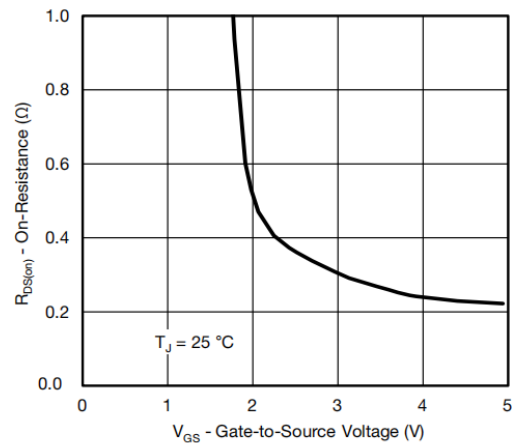
Output Characteristics



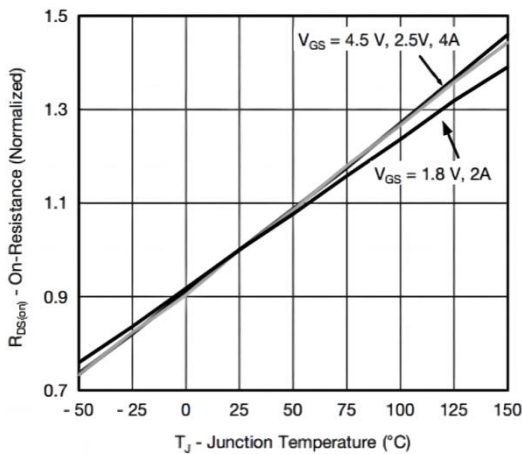
Transfer Characteristics



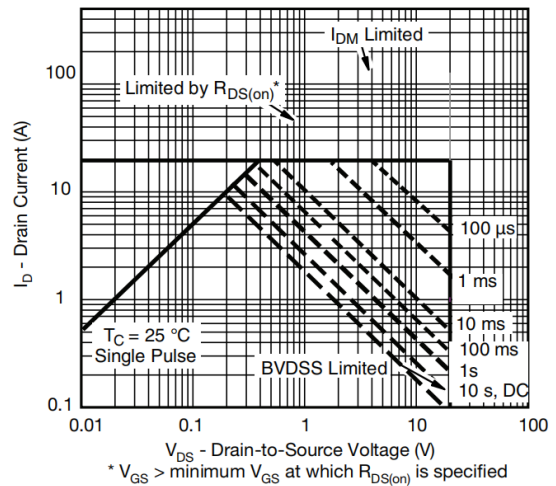
On-Resistance vs. Drain Current



On-Resistance vs. Gate-to-Source Voltage (2.4 A)

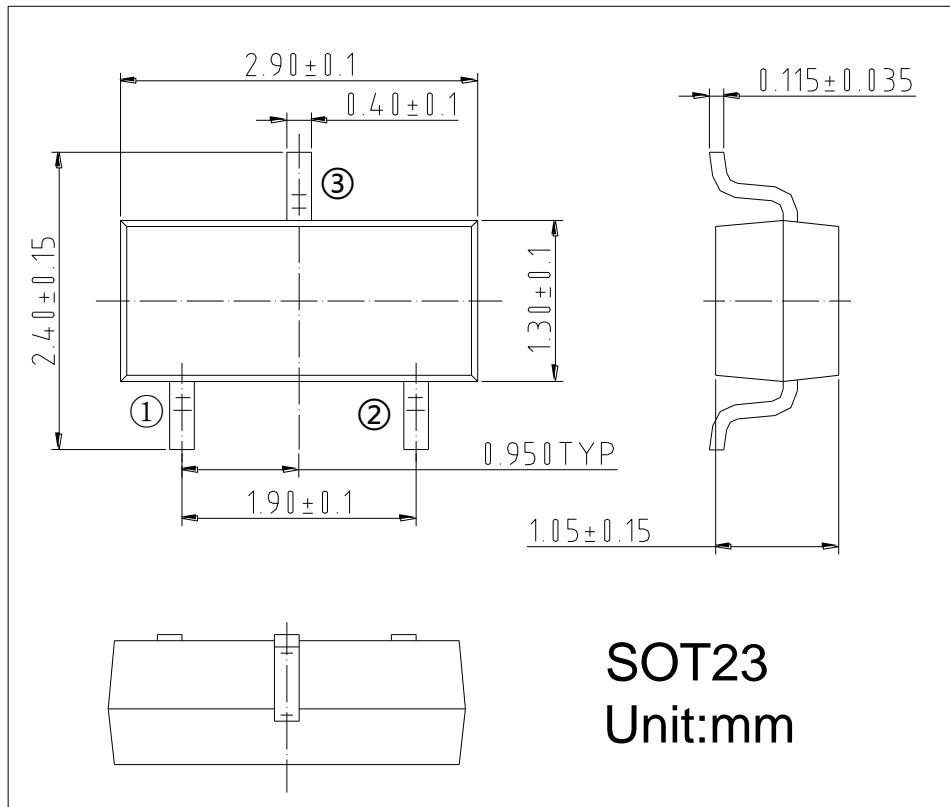


On-Resistance vs. Junction Temperature

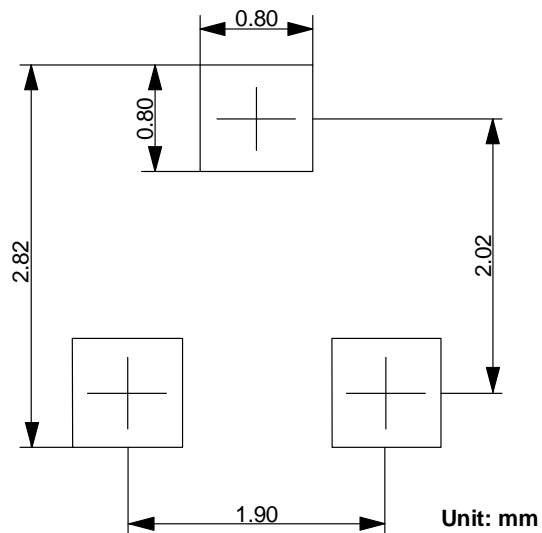


* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

➤ Package Information



➤ Recommended Pad outline





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