

SSCP123GS6

PNP Type Digital Transistor (built-in resistors)

> Features

vcc	VIN	ю	R1	R2/R1 Typ.
-50V	-12~+5V	-0.1A	2.2kΩ	21

> Description

Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).

The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects. Only the on/off conditions need to be set for operation, making the device design easy.

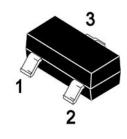
> Applications

- Amplifying signal
- Electronic switch
- Oscillating circuit
- Variable resistance

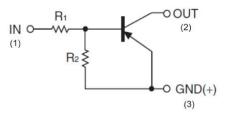
> Ordering Information

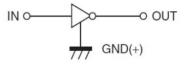
Device	Package	Shipping	
SSCP123GS6	SOT-23	3000/Reel	

Pin configuration



<u>SOT-23</u>





Circuit Diagram





> Absolute Maximum Ratings($T_A=25^{\circ}C$ unless otherwise noted)

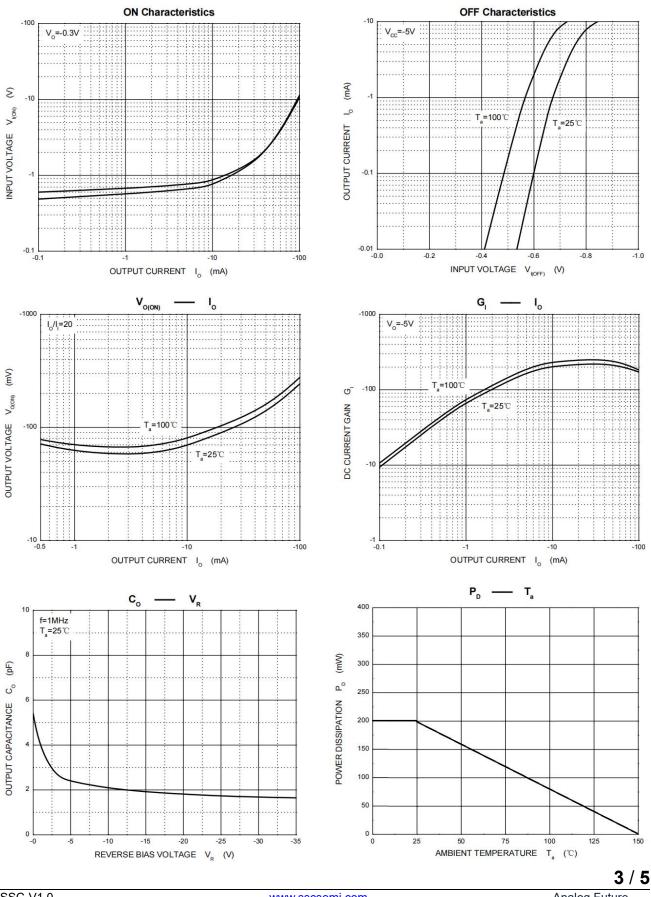
Parameter	Symbol	Value	Unit
Supply Voltage	V _{CC}	-50	V
Input Voltage	V _{IN}	-12 to +5	V
Output current	lo	-100	mA
Power Dissipation	PD	200	mW
Junction Temperature	TJ	-55 to 150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

> Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Input Voltage	V _{I(off)}	V_{CC} = -5V, I_{O} = -0.1mA	-0.5			V
Input Voltage	V _{I(on)}	V_{CC} = -0.3V, I_{O} = -5mA			-1.1	V
Output Voltage	V _{O(on)}	I ₀ /I ₁ = -5mA/-0.25mA		-0.1	-0.3	V
Input Current	lı	V1 = -5V			-3.6	mA
Output Current	I _{O(off)}	$V_{\rm CC} = -50V, V_{\rm I} = 0V$			-0.5	uA
DC Current Gain	G1	V ₀ = -5V, I ₀ = -10mA	80			
Input Resistance	R ₁		1.54	2.2	2.86	ΚΩ
Resistance Ration	R ₂ /R ₁		17	21	26	
Transition Frequency	f⊤	V ₀ =-10V, I ₀ =-5mA, f=100MHz		250		MHz



> Typical Performance Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)



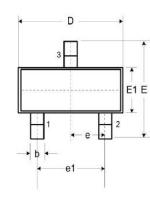


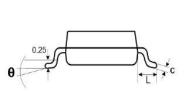


Package Information

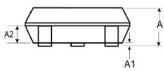
• Mechanical Data

<u>SOT-23</u>

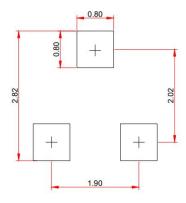




DIM	Millimeters				
DIM	Min.	Тур.	Max.		
Α	0.89	-	1.12		
A1	0.01	-	0.10		
A2	0.88	0.95	1.02		
b	0.30	-	0.51		
С	0.08	-	0.18		
D	2.80	2.90	3.04		
E	2.10	2.37	2.64		
E1	1.20	1.30	1.40		
е	0.95				
e1	1.90				
L	0.40	0.50	0.60		
L1		0.55			
N		3			
θ	0°	-	8°		



• Recommended Pad outline (Unit: mm)





DISCLAIMER

SSCSEMI RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. SSCSEMI DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICIENCE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

THE GRAPHS PROVIDED IN THIS DOCUMENT ARE STATISTICAL SUMMARIES BASED ON A LIMITED NUMBER OF SAMPLES AND ARE PROVIDED FOR INFORMATIONAL PURPOSE ONLY. THE PERFORMANCE CHARACTERISTICS LISTED IN THEM ARE NOT TESTED OR GUARANTEED. IN SOME GRAPHS, THE DATA PRESENTED MAY BE OUTSIDE THE SPECIFIED OPERATING RANGE (E.G. OUTSIDE SPECIFIED POWER SUPPLY RANGE) AND THEREFORE OUTSIDE THE WARRANTED RANGE.

OUR PRODUCT SPECIFICATIONS ARE ONLY VALID IF OBTAINED THROUGH THE COMPANY'S OFFICIAL WEBSITE, CRM SYSTEM, OR OUR SALES PERSONNEL CHANNELS. IF CHANGES OR SPECIAL VERSIONS ARE INVOLVED, THEY MUST BE STAMPED WITH A QUALITY SEAL AND MARKED WITH A SPECIAL VERSION NUMBER TO BE VALID.